



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **SUGATANI, Shinji, et al.**

Group Art Unit: **2814**

Serial No.: **10/634,839**

Examiner: **Howard Weiss**

Filed: **August 6, 2003**

P.T.O. Confirmation No.: 8605

For. **MANUFACTURING METHOD OF SEMICONDUCTOR DEVICE AND  
SEMICONDUCTOR CHIP USING SOI SUBSTRATE**

**PETITION FOR EXTENSION OF TIME**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Date: February 22, 2005

Sir:

Applicants petition the Commissioner for Patents to extend the time for response to the Office Action dated September 22, 2004 for partial two-months, from December 22, 2004 to February 22, 2005.

Attached please find a check in the amount of \$1,120.00 to cover the cost of \$790.00 for a Request for Continued Examination (RCE) and \$330.00 to cover the cost of the extension for a large entity. In the event that any additional fees are due in connection with this paper, please charge our Deposit Account No. 01-2340.

02/24/2005 HAHMED1 00000069 10634839

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330.00 DP

Respectfully submitted,  
ARMSTRONG, KRATZ, QUINTOS,  
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**23850**

PATENT TRADEMARK OFFICE

Enclosure: Check in amount of \$1,120.00